Reducing Project Lifecycle Cost with exSILentia

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exida
80 N. Main St.
Sellersville, PA
www.exida.com

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Abstract

The international functional safety standard IEC 61511 provides the safety lifecycle as a steadfast guideline to assess and mitigate risk for manufacturing processes including refineries, chemical, petrochemical, pulp and paper, and power plants. To achieve a functionally safe system, it is essential to follow each requirement in the standard. However, consistent execution is difficult to achieve and often depends on the tools used to perform analysis and specification of the safety instrumented system. For the functional safety consultants at exida, the need for a consistent work process was fulfilled with the creation of the exSILentia software suite. exSILentia includes a module for each stage of the safety lifecycle. Use of the tool ensures quality assessment and execution of a safety instrumented system, as well as compliance to the safety standard. exSILentia also streamlines these tasks, easily transferring data from one module to another to save the user time and money.

In this paper, the benefit of using exSILentia versus use of excel spreadsheets or other in-house tools is quantified. The intent is to show how users of the software reduce the number of engineering hours, and therefore dollars spent, for each safety lifecycle task. It is assumed that all required information is available when needed. Through conservative estimates, this paper proves that it pays to use exSILentia to support your safety lifecycle tasks and to make safety a priority.

Conclusion

It should be a top priority throughout the process industry to perform high quality analysis, implementation and operation of a safety instrumented system. To prove compliance to a functional safety standard like IEC 61511, it is important that the information be organized, accurate and properly documented. exSILentia provides the tools to easily perform and document all SLC tasks, while at the same time improving overall efficiency, and therefore saving time and money. This analysis highlights how use of exSILentia can impact the bottom line of each new project.

In the end, analyzing 10 nodes and subsequently analyzing, implementing, and maintaining 50 SIFs using excel or an in-house tool will take a grand total of approximately 2,000 hours. For users of exSILentia these same tasks should take about 600 hours. Depending on the hourly rate of the engineers assigned to each task, exSILentia will save $120K-$240K per 10 nodes and 50 SIFs. It is possible for a system in the process industry to have hundreds of nodes and SIFs. Based on the analysis documented in this paper, we can assume that use of excel or an in-house tool is nearly 4 times more expensive then use of the complete exSILentia suite.

Item	*Hours Spent - Using Excel*	*Hours Spent - Using exSILentia*	*Time/Cost Delta*
SLC Analysis Phase	990.5	476.0	514.5
SLC Realization Phase	900.0	100.5	799.5
SLC Operation & Maintenance Phase	332.0	29.0	303.0
Grand Total	2222.5	605.5	1617.0
Cost (Hourly Rate: $75/hour)	$166,687.50	$45,412.50	$121,275.00
Cost (Burdened Rate: $150/hour)	$333,375.00	$90,825.00	$242,550.00

Introduction

exSILentia provides a suite of tools that guide users through the analysis, realization, and operation phases of the safety lifecycle, as defined in IEC 61511. These phases include the following key tasks:

✓ Analysis Phase:

- Scope Definition and Process Design
- Process Hazard Analysis (PHA)
- Layer of Protection Analysis (LOPA)
- Safety Integrity Level (SIL) Selection • Safety Requirement Specification (SRS)

✓ Realization Phase:

- Safety Integrity Level (SIL) Verification
- Detailed Design Safety Requirement Specification (Design SRS)
- Programming of the PLC
- Specification of Proof Tests

✓ Operation Phase:

- Configuring Safety Instrumented System (SIS) into field collection database
- Field Failure and Proof Test Recording
- Standard Compliance, Audit Preparedness

Use of excel or an in-house tool may seem like the cheapest solution to support these SLC tasks and design a safety instrumented system. However, with each phase of the lifecycle comes a hefty to-do list that requires hours of preparation, discussion and documentation. As hours add up, the cost of the project increases. Use of exSILentia reduces the hours required for each task significantly by organizing and transferring inputs from one step to the next, providing built-in failure rate data, performing design calculations and generating necessary reports.

In the following sections, each task is described and an estimated time to complete the tasks using excel versus using exSILentia is provided. The time estimate for each task assumes 10 nodes are analyzed, each

resulting in 5 safety instrumented functions (SIF). To attribute a cost range to the hours spent, an hourly rate of $75 is assumed, as well as a burdened rate of $150 per hour.

Safety Lifecycle Phase 1: Analysis

Scope Definition and Process Design

To conduct a quality process hazard analysis, participants must be equipped with preliminary piping and instrumentation diagrams, equipment layouts, manning arrangements and safety targets. In short, the scope and design of the system must be well defined before any sessions are scheduled.

In some cases, PHA, LOPA, and SRS files from old projects can be used to expedite preparation for a new project. In addition, any failure data recorded at an existing site can be used as a reference. For the PHA and LOPA, a life event recorder may help determine the actual frequency of a process demand.

However, as a conservative estimate for the cost analysis we have assumed users of exSILentia and excel alike will have to start from scratch. Therefore, no cost estimate is provided for the scope definition.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Total Hours	Unit Total	Total Hours	Unit Total
Scope Definition and Process Design	-	-	-	-

Process Hazard Analysis (PHA)

To prepare for a PHA, the process plant must be broken down into smaller pieces called nodes. Nodes are typically small sections of the plant with a specific design intent. For example, a steam drum, piping feed into a reactor, a flare, and so on. For each node, different challenges to the process parameters are analyzed. These challenges are called deviations, and can include high pressure, low pressure, no flow, reverse flow, etc. Nodes and deviations must be defined before any sessions take place. exSILentia reduces this preparation time with embedded smart deviations for each node type. For this reason, preparation may take 0.3 hours per node using an in-house tool, but will only take 0.1 hours per node in exSILentia.

The objective of the PHA is to imagine all causes and consequences of a deviation to the process parameters. Risk is determined by quantifying the frequency of the cause, and the severity of the consequence. If the deviation potentially leads to a dangerous hazard, safeguards and recommendations are identified.

For quality analysis, input must be given from many perspectives. Most often, these sessions will include process engineers, process control engineers, safety engineers, operations and maintenance engineers, as well as a facilitator and a scribe. Depending on the size of the system in question, the PHA could require multiple sessions. The cost estimate for the PHA assumes five participants would spend 6 hours analyzing one node using an in-house tool, and 4 hours per node using the PHAx module in exSILentia. The benefit of using the tool's smart deviations and built-in libraries increase as more nodes are analyzed. To analyze a unit of ten nodes, exSILentia would save nearly 100 hours.

SLC Task	Hours spent - using excel		Hours spent - using exSILentia	
	Hours per node	Unit Total (10 nodes)	Hours per node	Unit Total (10 nodes)
Process Hazard Analysis (PHA)	30.3	303.0	20.1	201.0

Layer of Protection Analysis (LOPA)

The LOPA defines protection measures necessary to reduce the frequency of a dangerous hazard. The groundwork for this analysis is completed in the PHA. Safeguards identified in the PHA are analyzed as independent protection layers (IPL). The frequency of an initiating event is multiplied by the probability of failure of each protection layer, bringing the actual frequency of the hazard to a tolerable level. The protection layers can include anything from an alarm and operator intervention, basic process control function, a device such as a relief valve, or a safety instrumented function. Proper analysis requires a process engineer, a process control engineer and a safety engineer at a minimum.

In exSILentia useful information is transferred from the PHA module to the LOPA instantly, with the push of a button. In addition, the user can select applicable initiating event frequencies and probability of failure on demand for IPL's straight from the LOPA database in the tool. For this reason, preparation for a LOPA may take 3 hours per hazard scenario using an in house tool. However, hours needed to prepare using exSILentia are negligible.

This cost estimate assumes each node analyzed in the PHA has five hazard scenarios to be analyzed in the LOPA. In this case, one hazard scenario will take 2 hours using an in-house tool, but only 1 hour using exSILentia. If three engineers are required to perform the LOPA and they analyze 50 hazard scenarios, use of exSILentia would save 300 engineering hours.

SLC Task	Hours spent - using excel		Hours spent - using exSILentia	
	Total Hours per hazard scenario	Unit Total (50 hazard scenarios)	Total Hours per hazard scenario	Unit Total (50 hazard scenarios)
Layer of Protection Analysis (LOPA)	10.5	525.0	4.5	225.0

Safety Integrity Level (SIL) Selection

If the LOPA concludes a SIF is necessary to reach the target frequency for a hazard scenario, the risk reduction factor (RRF) and the safety integrity level (SIL) for that SIF must be defined before design and implementation. For each SIF, the RRF is the ratio of the actual frequency of the hazard divided by its target frequency. The value of this factor correlates to a safety integrity level as shown in the chart below.

Safety Integrity Level (SIL)	*Target average probability of failure on demand (PFD_{AVG})*	*Target Risk Reduction (RRF)*

4	≥ 10^{-5} to < 10^{-4}	> 10,000 to ≤ 100,000
3	≥ 10^{-4} to < 10^{-3}	> 1,000 to ≤ 10,000
2	≥ 10^{-3} to < 10^{-2}	> 100 to ≤ 1,000
1	≥ 10^{-2} to < 10^{-1}	> 10 to ≤ 100

This is a relatively simple task, especially when high quality analysis is done in the PHA and LOPA. However, if the system requires many SIFs, the number of hours spent on this task add up. exSILentia performs the SIL selection calculations automatically based on the LOPA, which should save up to 15 minutes per SIF. Assuming each hazard scenario analyzed in the LOPA requires one SIF, about 12 hours can be saved by using exSILentia for SIL selection.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
SIL Selection	0.3	12.5	0.0	0.0

Safety Requirement Specification (SRS)

The safety requirement specification outlines the purpose and target SIL of each SIF. The specification should answer many questions, including the following:

- What is the safe state?
- What equipment needs to be protected?
- What actions must be taken?
- What is the response time of those actions?

This document summarizes findings from the entire analysis phase of the safety lifecycle, and becomes the guideline for design and realization. To write the SRS from scratch may take 3 hours per SIF. However, with use of exSILentia information from PHAx and LOPAx is pre-populated into the SRS tool. This automatically generates a report, with little more than 1 hour needed per SIF to customize as needed. For 50 SIFs, use of exSILentia can save 100 hours.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
Safety Requirement Specification (SRS)	3.0	150.0	1.0	50.0

Safety Lifecycle Phase 2: Realization

Safety Integrity Level (SIL) Verification

The realization phase of the lifecycle starts with SIL verification. In this task, SIFs are designed to meet their target SIL level with guidance from the SRS. Each SIF includes a combination of three types of devices: sensors, logic solvers, and final elements. The achieved SIL level of a safety instrumented function is the lowest value of the following factors:

- The SIL level based on PFD_{AVG} (in low demand applications) for the sum of all pieces of equipment in the SIF.
- The SIL level based on minimum architectural constraints of each element in the SIF.
- The SIL level based on systematic capability for each piece of equipment in the SIF.

Minimum architectural constraints are determined based on redundancy levels of the SIF. Users of exSILentia do this simply by modelling the SIF in SILver. In some cases, the quality of the failure rate data must be validated per IEC 61508 Route 2_H. In SILver, this compliance is confirmed through its calculation engine.

To demonstrate systematic capability, selected equipment must be IEC 61508 certified or a proven in use justification must be documented. In exSILentia, the tool will automatically consider IEC 61508 compliance and proven in use justification can be easily documented.

Finally, the PFD_{AVG} calculation is based on the failure rate and failure modes of each device, mission time, mean time to restore, probability of initial failure, redundancy, and proof test intervals and effectiveness. To gather this information and perform the calculation could easily take 8 hours per SIF. However, exSILentia's SILver module has industry failure data from exida's Safety Equipment Reliability Handbook (SERH) embedded in the tool. Users of the tool can model the SIF and specify the equipment by selecting from the SERH. With all the necessary data on hand, the tool uses a Markov Model basis to automatically calculate the achieved SIL level. If the selected equipment does not meet the target SIL level, it is a simply matter of selecting a different device model from the SERH and/or adjust one of more of the other conceptual design parameters. For these reasons, modelling one SIF in SILver takes approximately one hour. If modeling 50 SIF's, one can save 350 hours by utilizing exSILentia.

For more information on the PFD_{AVG} calculation, please see exida's whitepaper: *The Key Variables Needed for PFD_{AVG} Calculation*, available from the resource section of the exida website.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
SIL Verification	8.0	400.0	1.0	50.0

Detailed Design Safety Requirement Specification (Design SRS)

Once conceptual design of your SIF is completed in SILver, the Design SRS outlines how the SIF should be implemented. Hardware requirements are defined here, as well as logical relationship information between inputs and outputs. The Design SRS defines, among others:

- Application level diagnostics
- Analog signal health range
- Voting arrangements
- Repair time requirements
- Process connection requirements
- Auxiliary inputs and outputs

Writing a Design SRS from scratch may take approximately 3 hours per SIF. In exSILentia, most of the required information is input or calculated during SIL verification, and can be transferred to the Design SRS module from SILver. Additional information like auxiliary inputs and outputs can be defined and linked to existing library items easily. From there, the document is automatically generated. This should take the user only 0.5 hours per SIF. If one is documenting 50 SIFs, use of exSILentia will save 125 hours.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
SIL Verification	3.0	150.0	0.5	25.0

Programming of the PLC

With the detailed design complete, each SIF can be programmed into the PLC. Information from the Design SRS like inputs, outputs, voting arrangement, trip delays, etc., must be converted to application program function blocks. In many cases, this is completed one at a time. For the majority of SIFs this is a very simple, yet time consuming process averaging 4 hours per SIF.

For exSILentia and DeltaV SIS users, exida will release a new DeltaV SIS Configurator module that will automatically convert the SILver and Design SRS information into an application program. This will allow for significant time savings, with the ability to convert all SIFs in one import. In addition, the automatic conversion eliminates the need for a programmer to interpret the Design SRS information and the creation of intermediate logic diagrams like cause and effect matrices. With this module, programming of the PLC should take no more than 0.5 hours. For 50 SIFs, use of exSILentia should save almost 200 hours.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
Programming of the PLC	4.0	200.0	0.5	0.5

Apart from the man hour time savings, one should also expect a significant project execution time savings as the application program can be created once the design is complete. This is in contrast with typical current project execution where the application program is created while the design is still being finalized resulting in many design changes and updates needed to be made to the application program. This additional benefit is not included in the above estimates.

Specification of Proof Tests

The proof test interval and effectiveness for each device in a SIF are key variables in the SIL verification calculation. Based on these parameters, a user will need to define a specific proof test for each device. Manufacturers of IEC 61508 compliant equipment are required to publish a proof test in their safety manual. These must be collected and documented in one specification to guide operators through the proof test once the system is installed and online. On average, 3 hours per SIF are required to complete the proof test specification. However, users of exSILentia can automatically generate a report containing all proof tests for devices in the SERH, saving 2.5 hours per SIF in the process. For a total of 50 SIFs, exSILentia users will save 125 hours on proof test specification.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
Proof test Specification	3.0	150.0	0.5	25.0

Safety Lifecycle Phase 3: Operation and Maintenance

The final phase of the safety lifecycle is often overlooked. However, the tasks of the operation and maintenance phase are required for standard compliance, and to validate the SIL verification calculations in the conceptual design of each SIF. These tasks include recording process demands, device failures, proof test results, and completion of routine maintenance.

Configuring SIS into field collection database

Tracking field failures, proof tests, and routine maintenance is mandatory per IEC 61511. To properly keep track of all devices, physical device locations, maintenance activities and proof test due dates, a structured database is most effective. However, populating information into such a database can be a time consuming task taking on average 6 hours per SIF.

Users of exSILentia can import SIF information from SILver and the Design SRS into exida's life event recorder, SILstat. This one import will configure the plant hierarchy, device information, device locations, and procedures for proof tests and routine maintenance. This import will take 0.5 hours per SIF. To configure 50 SIFs, use of exSILentia will save 275 hours.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
Configuring SIS into database	6.0	300.0	0.5	25.0

Field Failure and Proof Test Recording

During normal operation, field failures, proof tests, and process demands must be recorded. Though it is expected that recording with SILstat will be easier than a home-grown database due to ease of use, this cost benefit analysis conservatively assumes an equal amount of time will be spent on this task. Therefore, no cost estimate is provided for the scope definition.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
Failure & Proof Test Recording	-	-	-	-

Proof of Standard Compliance (Audit Preparedness)

It is important to have the ability to prove compliance to safety standards such as IEC 61511 in the event of a safety audit. These can be random or as a result of an incident. At such a time, all relevant functional safety documentation will be reviewed. This includes PHA and LOPA reports, SRS, SIL Selection reports, SIL Verification reports, Design SRS and Proof Test Reports. Evidence of life event recording including proof tests, maintenance activities, failure recording must also be shown. Collection of this information can be quite challenging if not stored in a centralized location. For users of exSILentia, all necessary information is embedded in the exSILentia file. For this comparison, it is conservatively estimated that use of exSILentia will save nearly 30 hours when preparing for an audit.

SLC Task	*Hours spent - using excel*		*Hours spent - using exSILentia*	
	Hours per SIF	Unit Total (50 SIFs)	Hours per SIF	Unit Total (50 SIFs)
Proof of Standard Compliance	32.0	32.0	4.0	4.0

Revision History

Revision	*Description*	*Date*	*Author*
1.0	First Release	January 2017	KMH

Authors: Kate Hildenbrandt, Iwan van Beurden

exida – Who we are.

exida is one of the world's leading accredited certification and knowledge companies specializing in automation system cybersecurity, safety, and availability. Founded in 2000 by several of the world's top reliability and safety experts, exida is a global company with offices around the world. exida offers training, coaching, project-oriented consulting services, standalone and internet-based safety and cybersecurity engineering tools, detailed product assurance and certification analysis, and a collection of online safety, reliability, and cybersecurity resources. exida maintains a comprehensive failure rate and failure mode database on electrical and mechanical components, as well as automation equipment based on hundreds of field failure data sets representing over 350 billion unit operating hours.

exida Certification is an ANSI (American National Standards Institute) accredited independent certification organization that performs functional safety (IEC 61508 family of standards) and cybersecurity (IEC 62443 family of standards) certification assessments.

exida Engineering provides the users of automation systems with the knowledge to cost-effectively implement automation system cybersecurity, safety, and high availability solutions. The exida team will solve complex issues in the fields of functional safety, cybersecurity, and alarm management, like unique voting arrangement analysis, quantitative consequence analysis, or rare event likelihood analysis, and stands ready to assist when needed.

Training

exida believes that safety, high availability, and cybersecurity are achieved when more people understand the topics. Therefore, exida has developed a successful training suite of online, on-demand, and web-based instructor-led courses and on-site training provided either as part of a project or by standard courses. The course content and subjects range from introductory to advanced. The exida website lists the continuous range of courses offered around the world.

Knowledge Products

exida Innovation has made the process of designing, installing, and maintaining a safety and high availability automation system easier, as well as providing a practical methodology for managing cybersecurity across the entire lifecycle. Years of experience in the industry have allowed a crystallization of the combined knowledge that is converted into useful tools and documents, called knowledge products. Knowledge products include procedures for implementing cybersecurity, the Safety Lifecycle tasks, software tools, and templates for all phases of design.

Tools and Products for End User Support

- exSILentia® – Integrated Safety Lifecycle Tool

- PHAx™ (Process Hazard Analysis)
- LOPAx™ (Layer of Protection Analysis)
- SILAlarm™ (Alarm Management and Rationalization)
- SILect™ (SIL Selection and Layer of Protection Analysis)
- Process SRS (PHA based Safety Requirements Specification definition)
- SILver™ (SIL verification)
- Design SRS (Conceptual Design based Safety Requirements Specification definition)
- Cost (Lifecycle Cost Estimator and Cost Benefit Analysis)
- PTG (Proof Test Generator)
- SILstat™ (Life Event Recording and Monitoring)

- exSILentia® Cyber- Integrated Cybersecurity Lifecycle Tool
  - CyberPHAx™ (Cybersecurity Vulnerability and Risk Assessment)
  - CyberSL™ (Cyber Security Level Verification)

Tools and Products for Manufacturer Support

- FMEDAx (FMEDA tool including the exida EMCRH database)
- ARCHx (System Analysis tool; Hardware and Software Failure, Dependent Failure, and Cyber Threat Analysis)

For any questions and/or remarks regarding this White Paper or any of the services mentioned, please contact exida:

exida.com LLC

80 N. Main Street

Sellersville, PA, 18960

USA

+1 215 453 1720

+1 215 257 1657 FAX

info@exida.com